# MULTI-RATE OC-48/STM-16 SFP TRANSCEIVERS WITH DIGITAL DIAGNOSTICS

# TRXA48M Single Mode

### **Product Description**

The TRXA48M SFP series of multi-rate fiber optic transceivers with digital diagnostics monitoring functionality provide a quick and reliable interface for short reach (SR), intermediate reach (IR) and long reach (LR) applications. Diagnostics monitoring functionality (alarm and warning features) is integrated into the design via an I<sup>2</sup>C serial interface per the Multi-Source Agreement (MSA) SFF-8472, Rev. 9.4.

Products under this series are compatible with SONET/ SDH standards for OC-48/STM-16 (2.488Gb/s) SR-1/I-16 short reach, IR-1/S-16.1 & IR-2/S-16.2 intermediate reach and LR-1/L-16.1 & LR-2/L-16.2 long reach applications, OC-3/STM-1 (156Mb/s) and OC-12/STM-4 (622Mb/s) SR/I-1 and I-4 short reach applications; Gigabit Ethernet LX (1.25Gb/s) applications per IEEE 802.3; and Fibre Channel 200-SM-LC-L (2.125Gb/s) and 100-SM-LC-L (1.062Gb/s) applications per FC-PI standards. The transceivers support data rates ranging from 2.67Gb/s down to 125Mb/s. All modules satisfy Class I Laser Safety requirements in accordance with the U.S. FDA/CDRH and international IEC-60825 standards.

The TRXA48 multi-rate transceivers connect to standard 20-pad SFP connectors for hot plug capability. This allows the system designer to make configuration changes or maintenance by simply plugging in different types of transceivers without removing the power supply from the host system.

The transceivers have colored bail-type latches, which offer an easy and convenient way to release the modules. The latch is compliant with the SFP MSA.

The transmitter and receiver DATA interfaces are AC-coupled internally. LV-TTL Transmitter Disable control input and Loss of Signal output interfaces are also provided.

The transceivers operate from a single +3.3V power supply over an operating case temperature range of  $-5^{\circ}C$  to  $+70^{\circ}C$  or  $-40^{\circ}C$  to  $+85^{\circ}C$ . The housing is made of metal for EMI immunity.



#### Features

- ☑ Lead Free Design & Fully RoHS Compliant
- ☑ Operating Bit Rate of 125Mb/s to 2.67Gb/s
- ☑ Compatible with SONET/SDH OC-48/STM-16
- Compatible with Gigabit Ethernet LX
- ☑ Compatible with Fibre Channel 200-SM-LC-L and 100-SM-LC-L
- ☑ Digital Diagnostics through Serial Interface
- ☑ Internal Calibration for Digital Diagnostics
- ☑ APD Receiver for Long Reach Applications
- ☑ Eye Safe (Class I Laser Safety)
- ☑ Duplex LC Optical Interface
- ☑ Hot-pluggable
- ☑ TX Fault & Loss of Signal Outputs
- TX Disable Input

Parameter		Symbol	Minimum	Maximum	Units
Storage Temperature		T <sub>ST</sub>	- 40	+ 85	°C
On another a Case Terrare and the 1	"B" Option	Т	- 5	+ 70	
Operating Case Temperature	ing Case Temperature <sup>1</sup> "A" Option T <sub>OP</sub> -40	+ 85	°C		
Supply Voltage		V <sub>cc</sub>	0	+5.0	V
Maximum Input Optical Power (30 seconds max.)		P <sub>in,max</sub>	-	+ 3.0	dBm
Input Voltage		V <sub>IN</sub>	0	V <sub>cc</sub>	V
Lead Terminal Finish, Reflow Profile Limits and MSL		-	-	NA	-
<sup>1</sup> Measured on top side of SFP modu	le at the front center ven	t hole of the cage.		,	

**Absolute Maximum Ratings** 

n Onlink Company







# TRXA48M SINGLE MODE

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1360

1580

1335

1580

2.4

1.0

-

0.16

- 117

0.07

1.0

2.0

nm

nm

nm

dB

ns

dB/Hz

Ulp-p

dB

(Over Operating Case reinperature. $v_{cc} = 3.13 \pm 0.3477$ )						
Parameter		Symbol	Minimum	Typical	Maximum	Units
Operating Data Rate		В	0.125	-	2.67	Mb/s
Average Optical Output Power (coupled into single mode fiber), 50% duty cycle	SR-1	P <sub>o</sub>	- 8.0	-	- 3.0	
	IR-1, IR-2		- 5.0	-	0	dBm
	LR-1, LR-2		- 2.0	-	+ 3.0	
	OC-3/12/48 & 2.125Gb/s	- /-	8.2	-	-	dB

9.0<sup>1</sup>

1266

1430

1280

1500

-

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30

-

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1310

1550

1310

1550

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Compliant with Telcordia GR-253-CORE and ITU-T Recommendation G.957

**Transmitter Performance Characteristics** (Over Operating Case Temperature. V<sub>cc</sub> = 3.13 to 3.47V)

Phi /Plo

 $\lambda_c$ 

 $\Delta\lambda_{RMS}$ 

 $\Delta\lambda_{20}$ 

SMSR

 $t_r$ ,  $t_f$ 

RIN

JG

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1.25Gb/s & 1.062Gb/s

SR-1, IR-1

IR-2

LR-1

LR-2

SR-1

IR-1, IR-2, LR-1, LR-2

IR-1, IR-2, LR-1, LR-2

OC-48

IR-1, IR-2, LR-1,

LR-2<sup>2</sup>

<sup>1</sup> Except for LR-2 which is 8.2dB minimum.

**Extinction Ratio** 

Center Wavelength

Spectral Width (RMS)

Spectral Width (-20dB)

Side Mode Suppression Ratio

**Relative Intensity Noise** 

Jitter Generation

**Dispersion Penalty** 

**Optical Output Eye** 

Optical Rise/Fall Time (20% to 80%)

<sup>2</sup> Specified at 1600ps/nm dispersion, which corresponds to the approximate worst-case dispersion for 80km G.652/G.654 fiber over the wavelength range of 1500-1580nm

**Receiver Performance Characteristics** (Over Operating Case Temperature. V<sub>cc</sub> = 3.13 to 3.47V)

Parameter		Symbol	Minimum	Typical	Maximum	Units			
Operating Data Rate		В	0.125	-	2.67	Gb/s			
		2.67Gb/s,	SR-1, IR-1, IR-2	Pmin	- 19.0	-	-	dBm	
Receiver Sensitivity (10 <sup>-10</sup> BER) <sup>1</sup>		LR-1, LR-2	- 28.0		-	-			
		OC-3/12			- 23.0	-	-		
		2.125Gb/s,	SR-1, IR-1, IR-2		- 20.0	-	-	dBm	
Receiver Ser (10 <sup>-12</sup> BER) <sup>2</sup>	sitivity	1.25Gb/s, 1.062Gb/s	LR-1, LR-2	Pmin	- 26.0	-	-		
		0.12	5Gb/s		- 23.0	-	-		
		SI	R-1		- 3.0	-	-	dBm	
Maximum Input Optical Power (10 <sup>-12</sup> BER)	IR-1	, IR-2 Pmax	Pmax	0	-	-			
	LR-1	, LR-2		- 8.0	-	-			
	SR-1, IR-1,	Increasing L	ight Input	Plos+	-	-	- 23.0		
LOS	IR-2	Decreasing	Light Input	Plos-	- 35.0	-	-	-ID	
Thresholds		Increasing L	ight Input	Plos+	-	-	- 28.0	dBm	
	LR-1, LR-2	Decreasing	Light Input	Plos-	- 42.0	-	-		
		Increasing L	ight Input	t_loss_off	-	-	100		
LOS Timing Delay Decreasing Light Input		t_loss_on	2.3	-	100	– μs			
LOS Hysteresis		-	0.5 <sup>3</sup>	-	-	dB			
Wavelength of Operation		λ	1260	-	1600	nm			
Receiver Reflectance		-	-	-	- 27.0	dB			
Moscurody	with 2 <sup>23</sup> -1 PRR								

<sup>1</sup> Measured with 2<sup>23</sup>-1 PRBS.

<sup>2</sup> Measured with 2<sup>7</sup>-1 PRBS.

<sup>3</sup> For OC-3, Minimum Hysteresis is 0.2dB.

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**Transmitter Performance Characteristics** (Over Operating Case Temperature.  $V_{cc} = 3.13$  to 3.47V)

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Parameter	Symbol	Minimum	Typical	Maximum	Units	
Input Voltage Swing (TD+ & TD-) <sup>1</sup>	$V_{_{PP-DIF}}$	0.35	-	1.75	V	
Input HIGH Voltage (TX Disable) <sup>2</sup>	V <sub>IH</sub>	2.0	-	V <sub>cc</sub>	V	
Input LOW Voltage (TX Disable) <sup>2</sup>	V <sub>IL</sub>	0	-	0.8	V	
Output HIGH Voltage (TX Fault) <sup>3</sup>	V <sub>OH</sub>	2.0	-	V <sub>cc</sub> + 0.3	V	
Output LOW Voltage (TX Fault) <sup>3</sup>	V <sub>OL</sub>	0	-	0.8	V	
<ul> <li><sup>1</sup> Differential peak-to-peak voltage.</li> <li><sup>2</sup> There is an internal 4.7 to 10kΩ pull-up resistor to V<sub>cc</sub>T.</li> <li><sup>3</sup> Open collector compatible, 4.7 to 10kΩ pull-up resistor to V<sub>cc</sub> (Host Supply Voltage).</li> </ul>						

**Receiver Electrical Interface** 

(Over Operating Case Temperature.  $V_{cc} = 3.13$  to 3.47V))

Parameter	Symbol	Minimum	Typical	Maximum	Units		
Output Voltage Swing (RD+ & RD-) <sup>1</sup>	V <sub>PP-DIF</sub>	0.40	-	1.75	V		
Output HIGH Voltage (LOS) <sup>2</sup>	V <sub>OH</sub>	V <sub>cc</sub> - 1.3	-	V <sub>cc</sub> + 0.3	V		
Output LOW Voltage (LOS)) <sup>2</sup>	V <sub>OL</sub>	0	-	0.5	V		
$^{\circ}$ Dran collector compatible 4.7 to 10kO pull up resistor to V. (Host Supply)(oltage)							

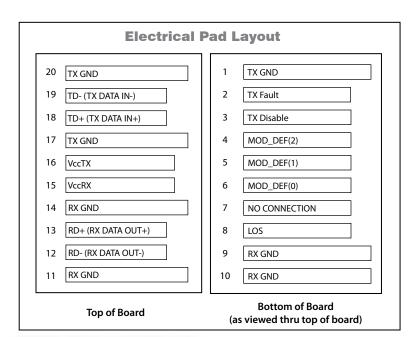
 $^2\,$  Open collector compatible, 4.7 to  $10k\Omega$  pull-up resistor to  $V_{_{CC}}$  (Host Supply Voltage).

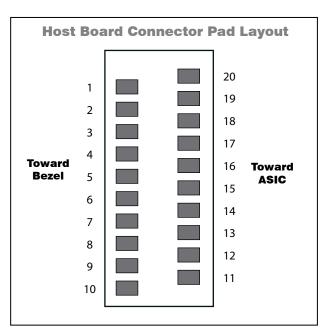
**Electrical Power Supply Characteristics** (Over Operating Case Temperature. V<sub>cc</sub> = 3.13 to 3.47V))

Parameter		Symbol	Minimum	Typical	Maximum	Units
Supply Voltage		V <sub>cc</sub>	3.13	3.3	3.47	V
Supply Current	SR-1, IR-1, IR-2	T	-	175	295	mA
Supply Current	LR-1, LR-2		-	175	350	

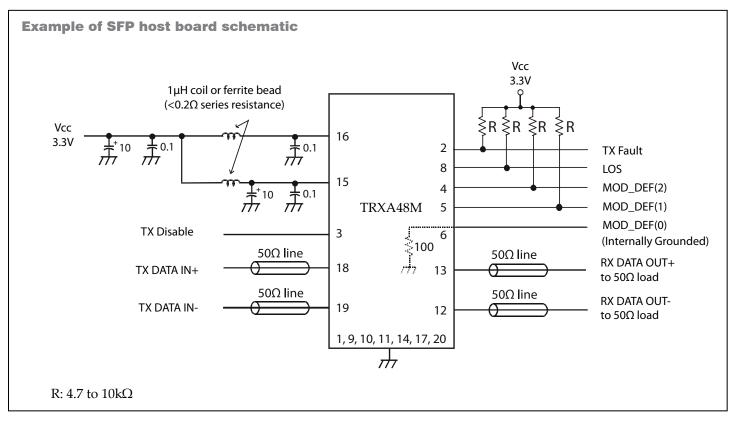
#### **Module Definition**

MOD_DEF(0)	MOD_DEF(1)	MOD_DEF(2)	Interpretation by Host
pin 6	pin 5	pin 4	
TTL LOW	SCL	SDA	Serial module definition protocol





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## **Application Notes**

**Electrical interface:** All signal interfaces are compliant with the SFP MSA specification. The high speed DATA interface is differential AC-coupled internally with 0.1µF and can be directly connected to a 3.3V SERDES IC. All low speed control and sense output signals are open collector TTL compatible and should be pulled up with a 4.7 - 10k $\Omega$  resistor on the host board

**Loss of Signal (LOS):** The Loss of Signal circuit monitors the level of the incoming optical signal and generates a logic HIGH when an insufficient photocurrent is produced.

**TX\_Fault:** The output indicates LOW when the transmitter is operating normally, and HIGH with a laser fault including laser end-of-life. TX Fault is an open collector/drain output and should be pulled up with a  $4.7 - 10k\Omega$  resistor on the host board. TX Fault in non-latching (automatically deasserts when fault goes away).

**TX\_Disable:** When the TX Disable pin is at logic HIGH, the transmitter optical output is disabled (less than -45dBm).

**Serial Identification and Monitoring:** The module definition of SFP is indicated by the three module definition pins, MOD\_DEF(0), MOD\_DEF(1) and MOD\_DEF(2).

#### **Laser Safety**

**Laser Safety:** All transceivers are Class I Laser products per FDA/CDRH and IEC-60825 standards. They must be operated under specified operating conditions.

Upon power up, MOD\_DEF(1:2) appear as NC (no connection), and MOD\_DEF(0) is TTL LOW. When the host system detects this condition, it activates the serial protocol (standard two-wire I<sup>2</sup>C serial interface) and generates the serial clock signal (SCL). The positive edge clocks data into the EEPROM segments of the SFP that are not write protected, and the negative edge clocks data from the SFP.

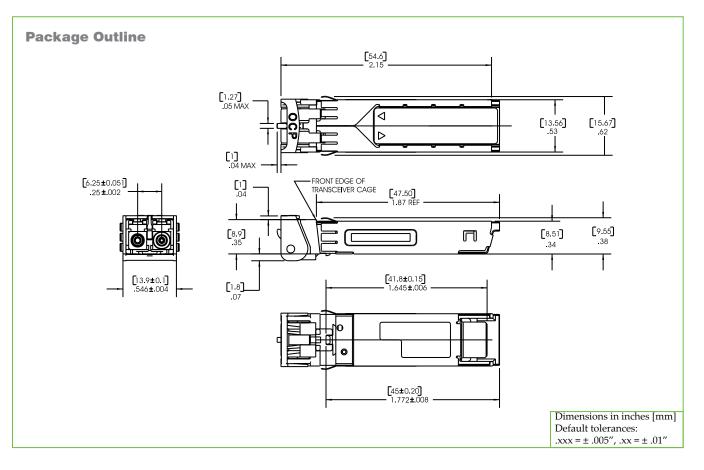
The serial data signal (SDA) is for serial data transfer. The host uses SDA in conjunction with SCL to mark the startand end of serial protocol activation. The supported monitoring functions are internal temperature, supply voltage, bias current, transmitter power, average receiver signal, all alarms and warnings and software monitoring of TX Fault/LOS. The device is internally calibrated.

The data transfer protocol and the details of the mandatory and vendor specific data structures are defined in the SFPMSA, and SFF-8472, Rev. 9.4

**Power supply and grounding:** The power supply line should be well-filtered. All  $0.1\mu$ F power supply bypass capacitors should be as close to the transceiver module as possible.

Oplink Communications, Inc. DATE OF MANUFACTURE: This product complies with 21 CFR 1040.10 and 1040.11 Meets Class I Laser Safety Requirements





## **Ordering Information**

Oplink can provide a remarkable range of customized optical solutions. For detail, please contact Oplink's Sales and Marketing for your requirements and ordering information (510) 933-7200 or Sales@oplink.com.

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